International
IOR Rectifier

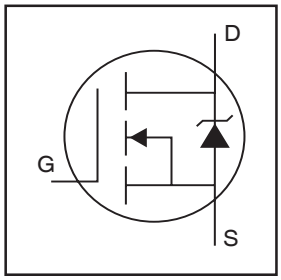
PDP MOSFET

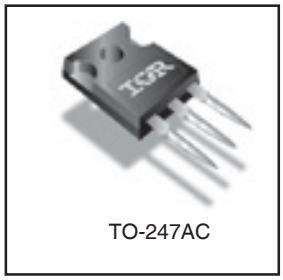
IRFP4232PbF

Features

- Advanced process technology
- Key parameters optimized for PDP Sustain & Energy Recovery applications
- Low E_{PULSE} rating to reduce the power dissipation in Sustain & ER applications
- Low Q_G for fast response
- High repetitive peak current capability for reliable operation
- Short fall & rise times for fast switching
- 175°C operating junction temperature for improved ruggedness
- Repetitive avalanche capability for robustness and reliability

Key Parameters		
V_{DS} min	250	V
$V_{DS\ (Avalanche)}$ typ.	300	V
$R_{DS(ON)}$ typ. @ 10V	30	mΩ
E_{PULSE} typ.	310	µJ
I_{RP} max @ T_C= 100°C	117	A
T_J max	175	°C





Description

This HEXFET® Power MOSFET is specifically designed for Sustain; Energy Recovery & Pass switch applications in Plasma Display Panels. This MOSFET utilizes the latest processing techniques to achieve low on-resistance per silicon area and low E_{PULSE} rating. Additional features of this MOSFET are 175°C operating junction temperature and high repetitive peak current capability. These features combine to make this MOSFET a highly efficient, robust and reliable device for PDP driving applications.

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{GS}	Gate-to-Source Voltage	±20	V
V_{GS} (TRANSIENT)	Gate-to-Source Voltage	±30	
I_D @ T_C = 25°C	Continuous Drain Current, V_{GS} @ 10V	60	A
I_D @ T_C = 100°C	Continuous Drain Current, V_{GS} @ 10V	42	
I_{DM}	Pulsed Drain Current ①	240	
I_{RP} @ T_C = 100°C	Repetitive Peak Current ⑤	117	
P_D @T_C = 25°C	Power Dissipation	430	W
P_D @T_C = 100°C	Power Dissipation	210	
	Linear Derating Factor	2.9	W/°C
T_J T_{STG}	Operating Junction and Storage Temperature Range	-40 to + 175	°C
	Soldering Temperature for 10 seconds	300	
	Mounting Torque, 6-32 or M3 Screw	10lb·in (1.1N·m)	N

Thermal Resistance

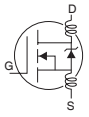
	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case ④	—	0.35	°C/W

Notes ① through ⑤ are on page 8

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	250	—	—	V	V_{GS} = 0V, I_D = 250µA
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	180	—	mV/°C	Reference to 25°C, I_D = 1mA
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	30	35.7	mΩ	V_{GS} = 10V, I_D = 42A ③
$V_{GS(th)}$	Gate Threshold Voltage	3.0	—	5.0	V	V_{DS} = V_{GS}, I_D = 250µA
$\Delta V_{GS(th)}/\Delta T_J$	Gate Threshold Voltage Coefficient	—	-15	—	mV/°C	
I_{DSS}	Drain-to-Source Leakage Current	—	—	5.0	µA	V_{DS} = 200V, V_{GS} = 0V
		—	—	150		V_{DS} = 200V, V_{GS} = 0V, T_J = 125°C
I_{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	V_{GS} = 20V
	Gate-to-Source Reverse Leakage	—	—	-100		V_{GS} = -20V
g_{fs}	Forward Transconductance	95	—	—	S	V_{DS} = 25V, I_D = 42A
Q_g	Total Gate Charge	—	160	240	nC	V_{DD} = 125V, I_D = 42A, V_{GS} = 10V③
Q_{gd}	Gate-to-Drain Charge	—	60	—		
$t_{d(on)}$	Turn-On Delay Time	—	37	—	ns	V_{DD} = 125V, V_{GS} = 10V ③
t_r	Rise Time	—	100	—		I_D = 42A
$t_{d(off)}$	Turn-Off Delay Time	—	64	—		R_G = 5.0Ω
t_f	Fall Time	—	63	—		See Fig. 22
t_{st}	Shoot Through Blocking Time	100	—	—	ns	V_{DD} = 200V, V_{GS} = 15V, R_G= 4.7Ω
E_{PULSE}	Energy per Pulse	—	310	—	µJ	L = 220nH, C= 0.4µF, V_{GS} = 15V V_{DS} = 200V, R_G= 4.7Ω, T_J = 25°C
		—	950	—		L = 220nH, C= 0.4µF, V_{GS} = 15V V_{DS} = 200V, R_G= 4.7Ω, T_J = 100°C
C_{iss}	Input Capacitance	—	7290	—	pF	V_{GS} = 0V
C_{oss}	Output Capacitance	—	610	—		V_{DS} = 25V
C_{rss}	Reverse Transfer Capacitance	—	240	—		ƒ = 1.0MHz, See Fig.5
C_{oss} eff.	Effective Output Capacitance	—	420	—		V_{GS} = 0V, V_{DS} = 0V to 200V
L_D	Internal Drain Inductance	—	5.0	—	nH	Between lead, 6mm (0.25in.)
L_S	Internal Source Inductance	—	13	—		from package and center of die contact

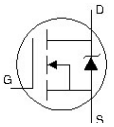


Avalanche Characteristics

	Parameter	Typ.	Max.	Units
E_{AS}	Single Pulse Avalanche Energy②	—	220	mJ
E_{AR}	Repetitive Avalanche Energy ①	—	43	mJ
$V_{DS(Avalanche)}$	Repetitive Avalanche Voltage ①	300	—	V
I_{AS}	Avalanche Current ②	—	42	A

Diode Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S @ T_C = 25°C	Continuous Source Current (Body Diode)	—	—	60	A	MOSFET symbol showing the
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	240		integral reverse p-n junction diode.
V_{SD}	Diode Forward Voltage	—	—	1.0	V	T_J = 25°C, I_S = 42A, V_{GS} = 0V ③
t_{rr}	Reverse Recovery Time	—	240	360	ns	T_J = 25°C, I_F = 42A, V_{DD} = 50V
Q_{rr}	Reverse Recovery Charge	—	1230	1850	nC	di/dt = 100A/µs ③

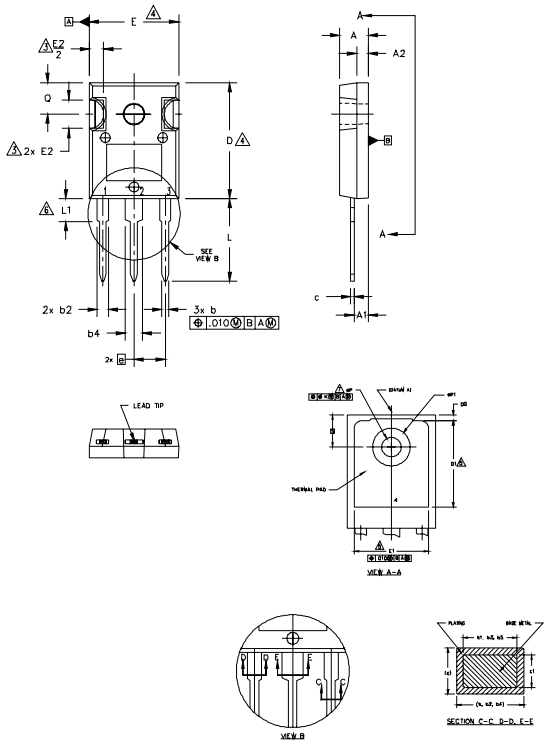


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TO-247AC Package Outline

Dimensions are shown in millimeters (inches)



NOTES:

1. DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M 1994.
2. DIMENSIONS ARE SHOWN IN INCHES.
3. CONTOUR OF SLOT OPTIONAL.
4. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
5. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS D1 & E1.
6. LEAD FINISH UNCONTROLLED IN L1.
7. øP TO HAVE A MAXIMUM DRAFT ANGLE OF 1.5 ° TO THE TOP OF THE PART WITH A MAXIMUM HOLE DIAMETER OF .154 INCH.
8. OUTLINE CONFORMS TO JEDEC OUTLINE TO-247AC .

SYMBOL	DIMENSIONS				NOTES
	INCHES		MILLIMETERS		
	MIN.	MAX.	MIN.	MAX.	
A	.183	.209	4.65	5.31	
A1	.087	.102	2.21	2.59	
A2	.059	.098	1.50	2.49	
b	.039	.055	0.99	1.40	
b1	.039	.053	0.99	1.35	
b2	.065	.094	1.65	2.39	
b3	.065	.092	1.65	2.34	
b4	.102	.135	2.59	3.43	
b5	.102	.133	2.59	3.38	
c	.015	.035	0.38	0.89	
c1	.015	.033	0.38	0.84	
D	.776	.815	19.71	20.70	4
D1	.515	–	13.08	–	5
D2	.020	.053	0.51	1.35	
E	.602	.625	15.29	15.87	4
E1	.530	–	13.46	–	
E2	.178	.216	4.52	5.49	
e	.215 BSC		5.46 BSC		
øk	.010		0.25		
L	.559	.634	14.20	16.10	
L1	.146	.169	3.71	4.29	
øP	.140	.144	3.56	3.66	
øP1	–	.291	–	7.39	
Q	.209	.224	5.31	5.69	
S	.217 BSC		5.51 BSC		

LEAD ASSIGNMENTS

HEXFET

1.– GATE
2.– DRAIN
3.– SOURCE
4.– DRAIN

IGBTs, CoPACK

1.– GATE
2.– COLLECTOR
3.– EMITTER
4.– COLLECTOR

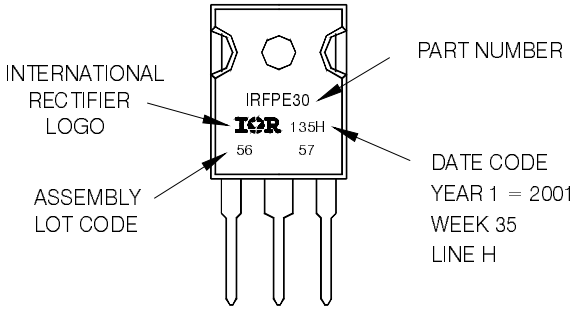
DIODES

1.– ANODE/OPEN
2.– CATHODE
3.– ANODE

TO-247AC Part Marking Information

EXAMPLE: THIS IS AN IRFPE30
WITH ASSEMBLY
LOT CODE 5657
ASSEMBLED ON WW 35, 2001
IN THE ASSEMBLY LINE "H"

Note: "P" in assembly line position
indicates "Lead-Free"



TO-247AC package is not recommended for Surface Mount Application.

Notes:

① Repetitive rating; pulse width limited by max. junction temperature.

② Starting T_J = 25°C, L = 0.25mH, R_G = 25Ω, I_{AS} = 42A.

③ Pulse width ≤ 400µs; duty cycle ≤ 2%.

④ R_θ is measured at T_J of approximately 90°C.

⑤ Half sine wave with duty cycle = 0.25, ton=1µsec.

Note: For the most current drawing please refer to IR website at: http://www.irf.com/package/

Data and specifications subject to change without notice.
This product has been designed for the Consumer market.
Qualification Standards can be found on IR's Web site.

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