N-Channel MOS FET

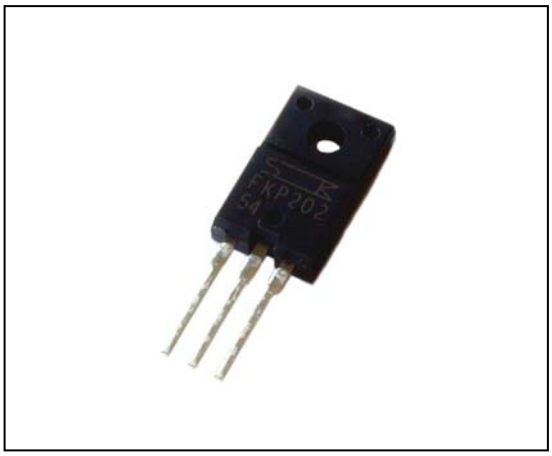
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■Features

- ●Low on-resistance
- ●Low input capacitance
- ●Avalanche energy capability guarantee

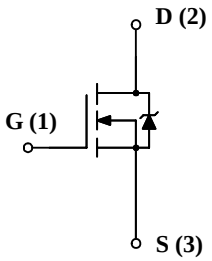
■Package---FM20 (TO220 Full Mold)



■Applications

- ●PDP driving
- ●High speed switching

■Equivalent circuit



■Absolute maximum ratings

(Ta=25°C)

Parameter	Symbol	Rating	Unit
Drain to Source Voltage	VDSS	200	V
Gate to Source Voltage	VGSS	±30	V
Continuous Drain Current	ID	±45A	A
Pulsed Drain Current	ID(pulse) *1	±180A	A
Maximum Power Dissipation	PD	40 (Tc=25°C)	W
Single Pulse Avalanche Energy	EAS *2	200	mJ
Avalanche Current	IAS	45	A
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	−55 to 150	°C

*1. PW≤100μs, duty cycle≤1%

*2. VDD=20V, L=180μH, ILp=45A, unclamped, RG=50Ω, See Fig.1

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Electrical characteristics

(Ta=25°C)

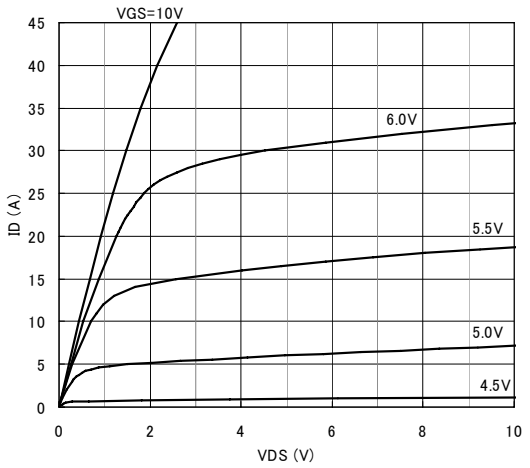
Parameter	Symbol	Test Conditions	Limits			Unit
			MIN.	TYP.	MAX.	
Drain to Source breakdown Voltage	V(BR)DSS	ID=100μA,VGS=0V	200			V
Gate to Source Leakage Current	IGSS	VGS=±30V			±100	nA
Drain to Source Leakage Current	IDSS	VDS=200V, VGS=0V			100	μA
Gate Threshold Voltage	VTH	VDS=10V, ID=1mA	3.0		4.5	V
Forward Transconductance	Re(Yfs)	VDS=10V, ID=22A	18	28		S
Static Drain to Source On-Resistance	RDS(on)	ID=22A, VGS=10V		45	53	mΩ
Input Capacitance	Ciss	VDS=25V VGS=0V f=1MHz		2000		pF
Output Capacitance	Coss			400		
Reverse Transfer Capacitance	Crss			80		
Turn-On Delay Time	td(on)	ID=22A, VDD≈100V RL=4.5Ω, VGS=10V RG=5Ω See Fig.2		30		ns
Rise Time	tr			100		
Turn-Off Delay Time	td(off)			90		
Fall Time	tf			50		
Source-Drain Diode Forward Voltage	VSD	ISD=45A,VGS=0V		1.0	1.5	V
Gate Threshold Voltage Temp. Coefficient	ΔVTH /ΔTch	VDS=10V, ID=1mA		-8		ns

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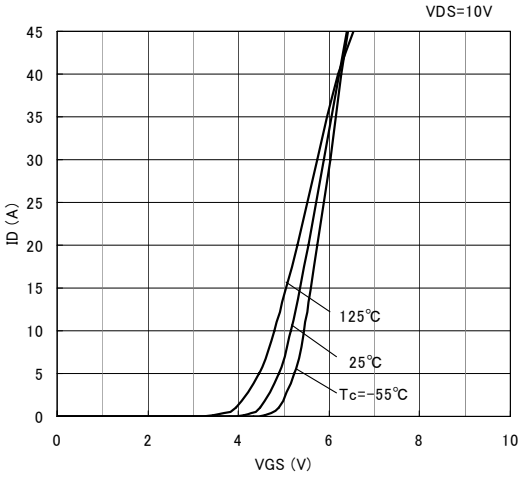
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Characteristic Curves (Tc=25℃)

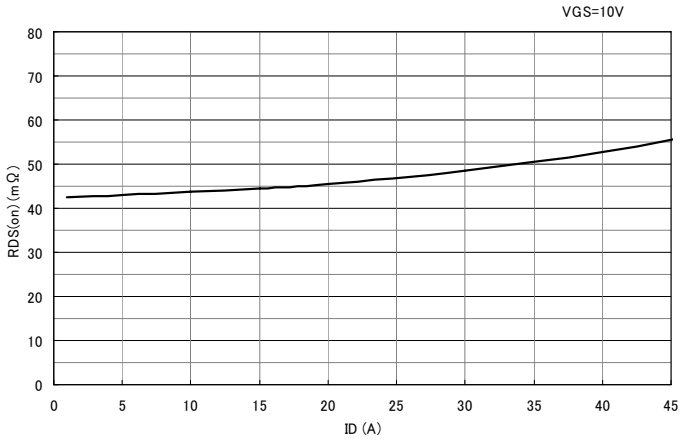
ID-VDS Characteristics (typical)



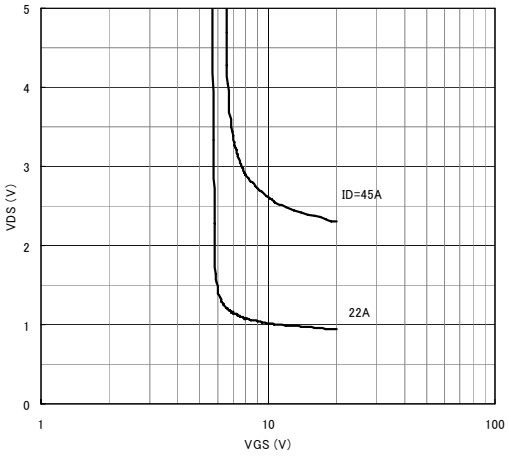
ID-VGS Characteristics (typical)



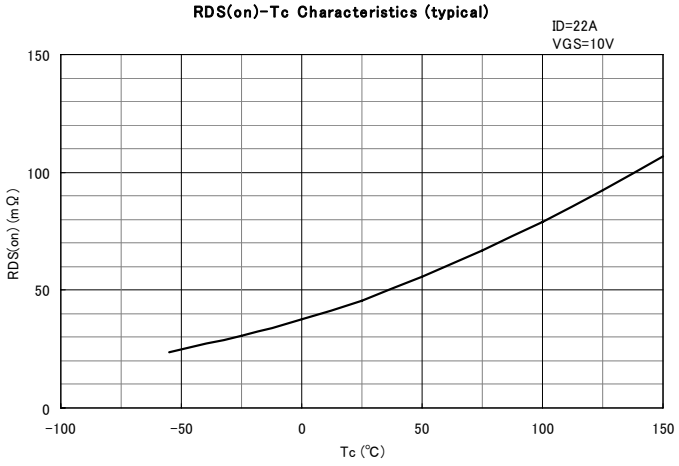
RDS(ON)-ID Characteristics (typical)



VDS-VGS Characteristics (typical)



RDS(on)-Tc Characteristics (typical)

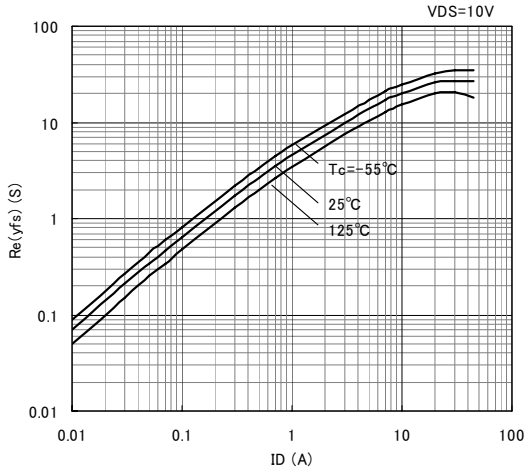


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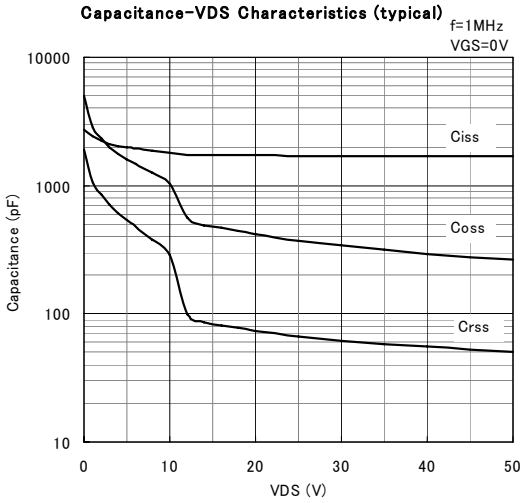
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Characteristic Curves (Tc=25℃)

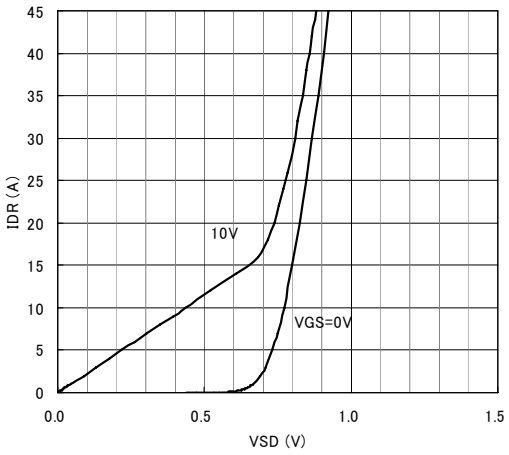
Re(yfs)-ID Characteristics (typical)



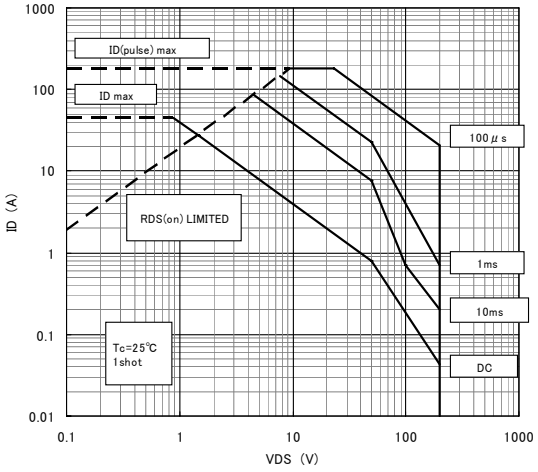
Capacitance-VDS Characteristics (typical)



IDR-VSD Characteristics (typical)



SAFE OPERATING AREA



PD-Ta Characteristics

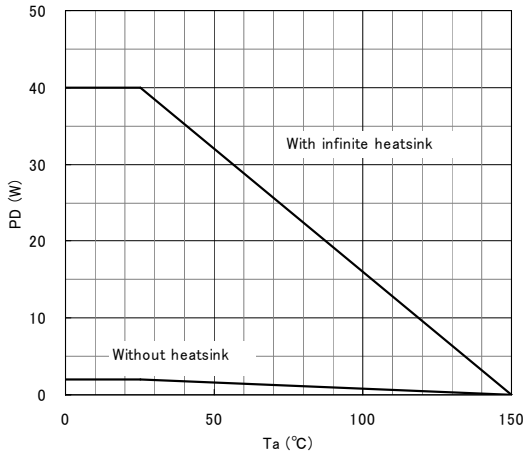
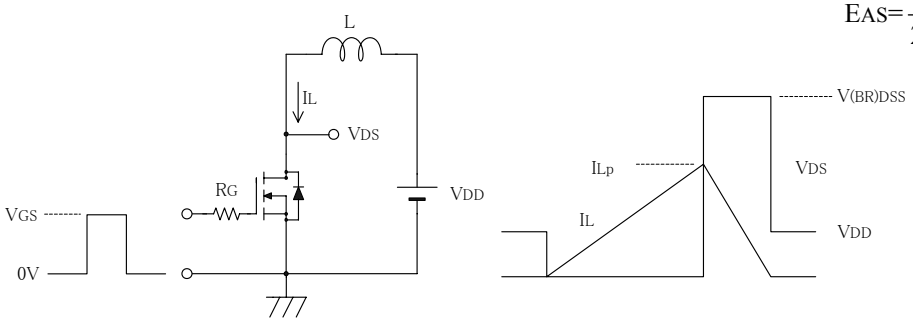


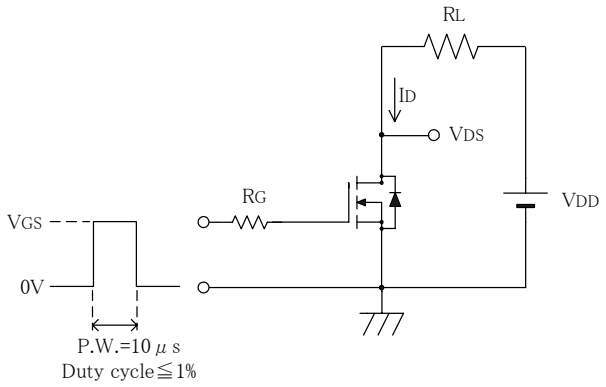
Fig.1 Unclamped Inductive Test Method

$$E_{AS}=\frac{1}{2}\cdot L\cdot I_{LP}^{2}\cdot\frac{V_{(BR)DSS}}{V_{(BR)DSS}-V_{DD}}$$



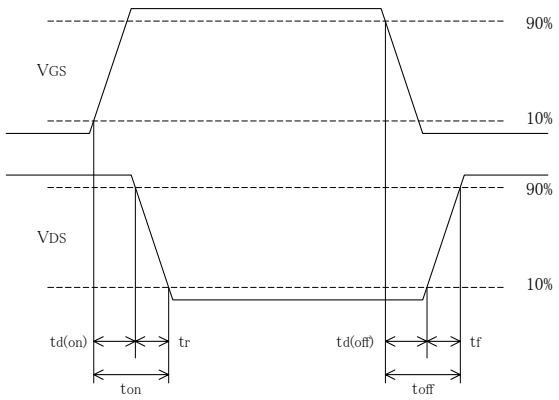
(a) Test Circuit (b) Waveforms

Fig.2 Switching Time Test Method



VDD≈100V
ID=22A
RL=4.5Ω
VGS=10V
RG=5Ω

(a) Test Circuit



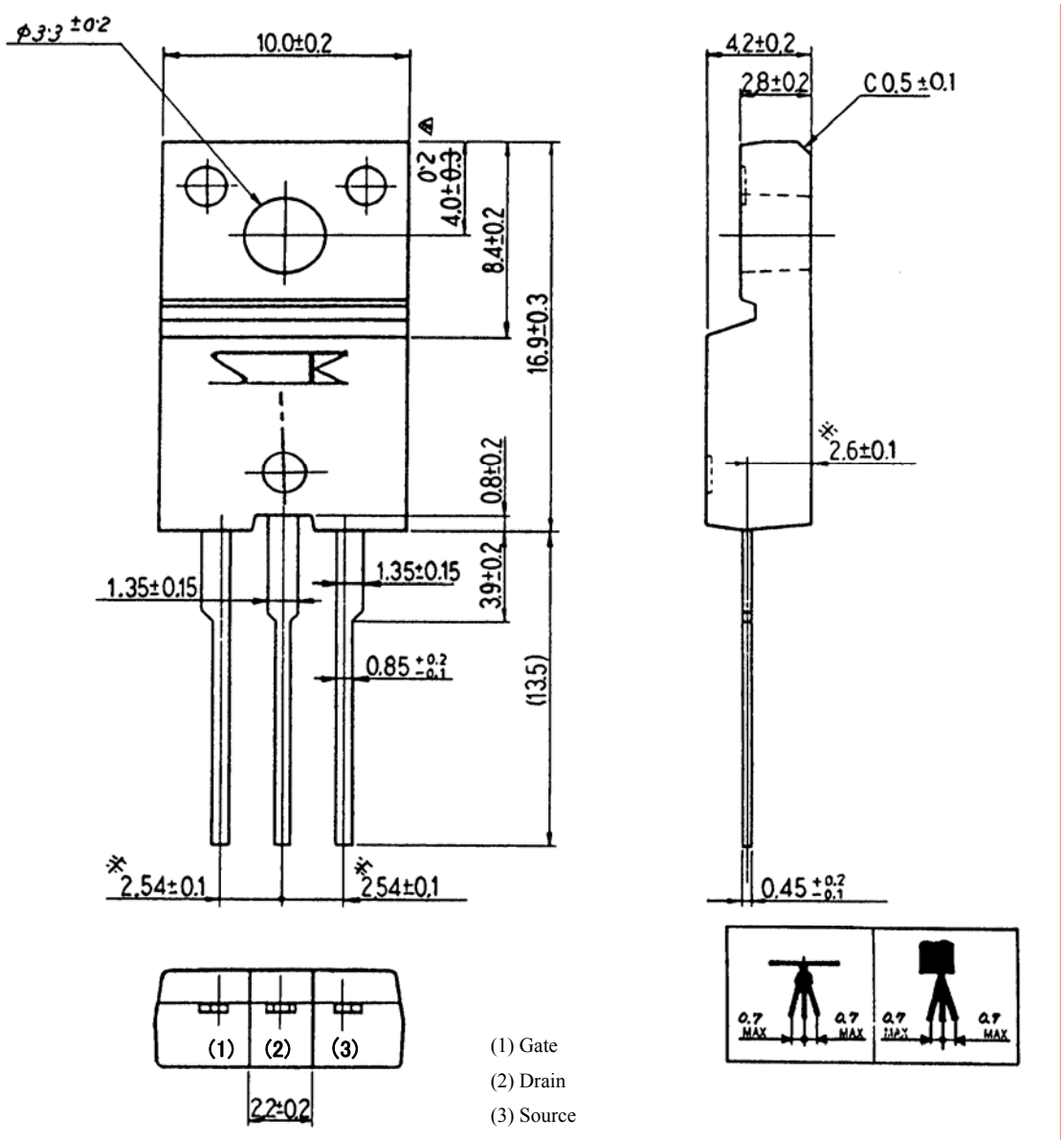
(b) Waveforms

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External dimensions

FM20 (TO220 Full Mold)



(1) Gate
(2) Drain
(3) Source

Weight Approx. 2g

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